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Attorney Docket No.: 00939H-081300US

Assistant Commissioner for Patents,  
Washington, D.C. 20231, on 3/3/03

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TOWNSEND and TOWNSEND and CREW LLP

By *Lata Olivier*

Lata Olivier

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Jong-Hee Han

Examiner: Hoai V. Ho

Application No.: 09/742,816

Art Unit: 18

Filed: December 19, 2000

LETTER TO OFFICIAL DRAFTSPERSON

For: DELAY LOCKED LOOP FOR USE IN  
SEMICONDUCTOR MEMORY  
DEVICE

Box Issue Fee

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Pursuant to the Notice of Allowability dated December 5, 2002, enclosed are ten (10) sheets of formal drawings to be made of record in the above-identified case.

Respectfully submitted,

*Cathy E. Cretsinger*

Cathy E. Cretsinger  
Reg. No. 51,588

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